

# UTILITY PATENT APPLICATION TRANSMITTAL

Attorney Docket No.: CFP00353 (20040144.ORI)  
First Inventor: Herng-Jer Lee, et al.  
Title: Method on Scan Chain Reordering for Lowering VLSI Power Consumption

Express Mail Label No.: EV373529185US

Address to: Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## Application Elements:

1. ☒ Applicant claims small entity status.  
See 37 CFR 1.27.
2. ☒ Specification Total Pages: 21
3. ☒ Drawings Total Sheets: 6
4. Oath or Declaration Total Pages: 2
  - a. ☒ Newly executed.
  - b. ☐ Copy from prior application  
(for continuation/divisional with Box 13 completed)
5. ☒ Application Data Sheet.

## Accompanying Application Parts

6. ☒ Assignment Papers (cover sheet & document)
7. ☐ Information Disclosure Statement
8. ☐ Preliminary Amendment
9. ☒ Return Receipt Postcard
10. ☐ Request and Certification Under 35 U.S.C. 122(b)(2)(B)(i)

11. ☒ Certificate of Mailing Via Express Mail
12. ☒ Check in the amount of \$425.00
13. ☐ If a **CONTINUING APPLICATION** (check appropriate box and supply requisite information below and in a preliminary amendment, or in an Application Data Sheets under 37 CFR 1.76):

☐ Continuation ☐ Divisional ☐ Continuation-in-Part

of prior application Serial No.:

Prior application information: Examiner: Group Art Unit:

☒ **Assignee Information for Inclusion on the Patent Application Publication:**

Assignee:: Chang Gung University  
Address:: 259, Wen-Hwa 1<sup>st</sup> Road, Kwei-Shan  
Tao-Yuan, TAIWAN, R.O.C.

#### Method of Payment


☒ Payment enclosed by check to cover the filing and recording fees.

☒ The Commissioner is authorized to charge any fees or refund any overpayment under 37 CFR 1.16 and 1.17 which may be required by this paper to Deposit Account No. 08-1265 under Deposit Account name of Nikolai & Mersereau, P.A.

#### Correspondence Address:

Customer No.: 23595

Typed Attorney Name: Alan D. Kamrath  
Attorney Registration No.: 28,227  
Telephone Number: 612-339-7461

Signature:  Date: April 19, 2004.

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Re App : Heng-Jer Lee, et al.

: April 19, 2004

For : Method on Scan Chain Reordering for Lowering VLSI Power Capability  
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**CERTIFICATE OF MAILING VIA EXPRESS MAIL**

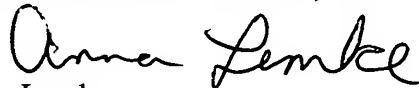
Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

I hereby certify that the attached patent application consisting of sixteen (16) pages of specification, four (4) pages of claims, abstract, one copy of six (6) sheets of formal patent drawings, an executed Declaration for Patent Application and Appointment of Attorney (2pp.), a Patent Application Data Entry Format sheet (2pp.), a Utility Patent Application Transmittal (2pp.), an executed Assignment Deed, a Recordation Form Cover Sheet (2pp.), a return receipt postcard and a check in the amount of \$425.00 in payment of the filing and recording fees, are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, under Express Mail Post Office to Addressee Label No. EV373529185US.

Respectfully submitted,

NIKOLAI & MERSEREAU, P.A.



Anna Lemke

On Behalf of Alan D. Kamrath

Attorney for Applicant(s)

900 Second Avenue South, #820

Minneapolis, MN 55402

Telephone: (612) 339-7461